Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

Claims 1-18 (Canceled)

Claim 19. (Previously Presented) A process for mobile reception of television signals comprising:

receiving a plurality of different input signals;

time synchronizing said input signals using video synchronization pulses contained in said input signals;

evaluating the quality of said input signals using at least one criterion for determining an adaptively determined weighting factor;

weighting said input signals;

deriving an output signal based on said weighted input signals; and

feeding said output signal to a receiver.

Claim 20. (Previously Presented) The process of claim 19, wherein said video synchronization pulses are selected from the group consisting of horizontal video synchronization pulses and vertical synchronization pulses

Claim 21. (Previously Presented) The process of claim 19, wherein said output signal is derived by summing said weighted input signals.

Claim 22. (Previously Presented) The process of claim 19, wherein said output signal is derived by summing said weighted input signals.

Claim 23. (Previously Presented) The process of claim 19, further comprising the step of delaying said input signals before deriving said output signal to allow determination of said weighting factors.

Claim 24. (Previously Presented) The process of claim 23, wherein a FIFO memory is used for said delay.

Claim 25. (Previously Presented) The process of claim 19, wherein said input signals are digital signals.

Claim 26. (Previously Presented) The process of claim 25, wherein each of said input signals is received with its own antenna and its own tuner.

Claim 27. (Previously Presented) The process of claim 26, wherein high frequency signals received with said antennas are already digitally modulated.

Claim 28. (Previously Presented) The process of claim 27, wherein said input signals comprise analog and digital signals having luminance and chrominance portions produced by video decoders serially connected to said tuners.

Claim 29. (Previously Presented) The process of claim 28, wherein said luminance and chrominance portions of each input signal is evaluated, weighted, and summed independently of one another.

Claim 30. (Previously Presented) The process of claim 19, wherein said criterion is selected from the group consisting of magnitude of noise level, signal-to-noise ratio of a signal level, and occurrence of interference.

Claim 31. (Previously Presented) The process of claim 30, wherein at least two of said criterion are used to evaluate the quality of said input signals.

Claim 32. (Previously Presented) The process of claim 19, wherein said criterion is based on a presence of a deterministic signal portion.

Claim 33. (Previously Presented) The process of claim 19, wherein said input signals are weighted relative to their rating as compared to the input signal having the highest rating for said criterion.

Claim 34. (Previously Presented) The process of claim 33, further comprising the step of setting a threshold rating relative to the input signal having the highest rating and assigning input signals falling below said threshold rating a weight factor of zero.

Claim 35. (Previously Presented) The process of claim 19, wherein said input signals are video signals and are received during said receiving step.

Claim 36. (Previously Presented) The process of claim 19, wherein after deriving said output signals, said output signals are buffered and undisrupted signals received before a period of interference are transmitted to said receiver.

Claim 37. (Previously Presented) The process of claim 19, further comprising the step of setting receiving paths for input signals not contributing to improving said output signal to different frequency ranges having the same information.

Claim 38. (Currently Amended) A circuit for allowing mobile reception of broadcast signals comprising:

a receiving means for receiving a plurality of different input signals;

a time synchronization means ~~unit~~ for time synchronization of the input signals using video synchronization pulses contained with the input signals [,] ~~said unit having a first FIFO memory for each signal~~;

~~a clock generator;~~

~~a synchronization control unit;~~

a signal conditioning means for evaluating the quality of the input signals using at least one criterion for determining an adaptively determined weighting factor ~~unit, wherein each synchronized input signal is subjected to a signal evaluation circuit and a following means for forming weighting factors, and wherein a second FIFO memory having a storage depth corresponding to a period of time of signal evaluation and formation of weighting factors~~;

a determining means for weighting the input signals;

a multiplication and summing means for forming output signals using the weighted input signals; and

a receiver having a reproduction part in communication with an output of said multiplication and summing means.